

Product Brief

Product Summary

The PC802 Evaluation Board (PC802EVB) is a PCIe development board used to evaluate the PC802 SoC silicon, bringing all the main PC802 interfaces to connectors. The Board has a 4-line Gen 4 PCIe interface, a 10G/25G Ethernet SFP+ interface and the baseband JESD204B lanes are brought to an FMC connector, suitable for connection to compatible RFIC vendor evaluation boards*.

The PC802EVB supports upper PHY processing for a O-RAN Alliance defined O-DU, by transforming the SCF FAPI messages from the L2 software sent over the PCIe interface into Open fronthaul eCPRI interface transmitted over the Ethernet interface. Full end to end evaluation testing can be achieved by interfacing to an O-RU radio unit or UE Emulator test equipment with O-RU capability.

The PC802 Evaluation Board also supports the full PHY using the PCIe FAPI interface and RFIC JESD204B interface*. Similarly, the PC802EVB can also be configured in O-RAN Alliance O-RU mode using the Open fronthaul eCPRI interface and JESD204B interface*.

**For customers wanting to evaluate use cases with ADI ADRV9029 RFIC, it is recommended that customers use the PicoCom PC802 Small Cell Development Board*

Software

Software Included

PC802EVB customers also purchase a software support package consists of:

- ◆ PCOMware (incl BSP, drivers, library)
- ◆ Limited binary demonstration software
- ◆ Support for demonstration software

This allows customers and partners to evaluate the PC802 silicon and software in combination with partner software and hardware over standardised interfaces.

Additional Software Supported

The PC802EVB hardware is also compatible with the following PicoCom software, which are separately licensed complete with support and roadmap:

- ◆ 5G NR PHY binary or source code
- ◆ 5G NR RU binary or source code
- ◆ LTE PHY binary or source code
- ◆ LTE RU PHY binary or source code

PC802EVB is also compatible with the following 3rd party tools to provide additional debug and source code development support if needed:

- ◆ Siemens Tessent SystemInsight
- ◆ Ceva Software Development Tools
- ◆ AndeSight Software Development Tools

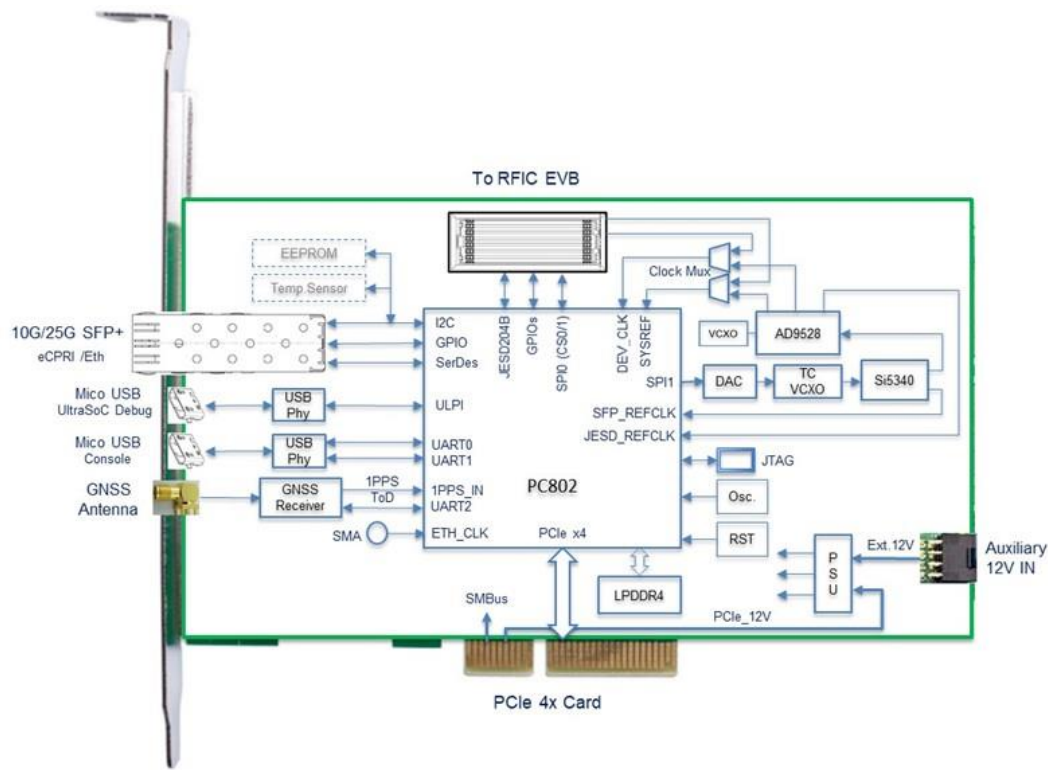
For more information on software products and tools, please contact PicoCom.





PC802 Evaluation Board Description

The board is a half-length, full-height PCIe 4x add-in PCIe card, and has the following features and interfaces.



Key Features

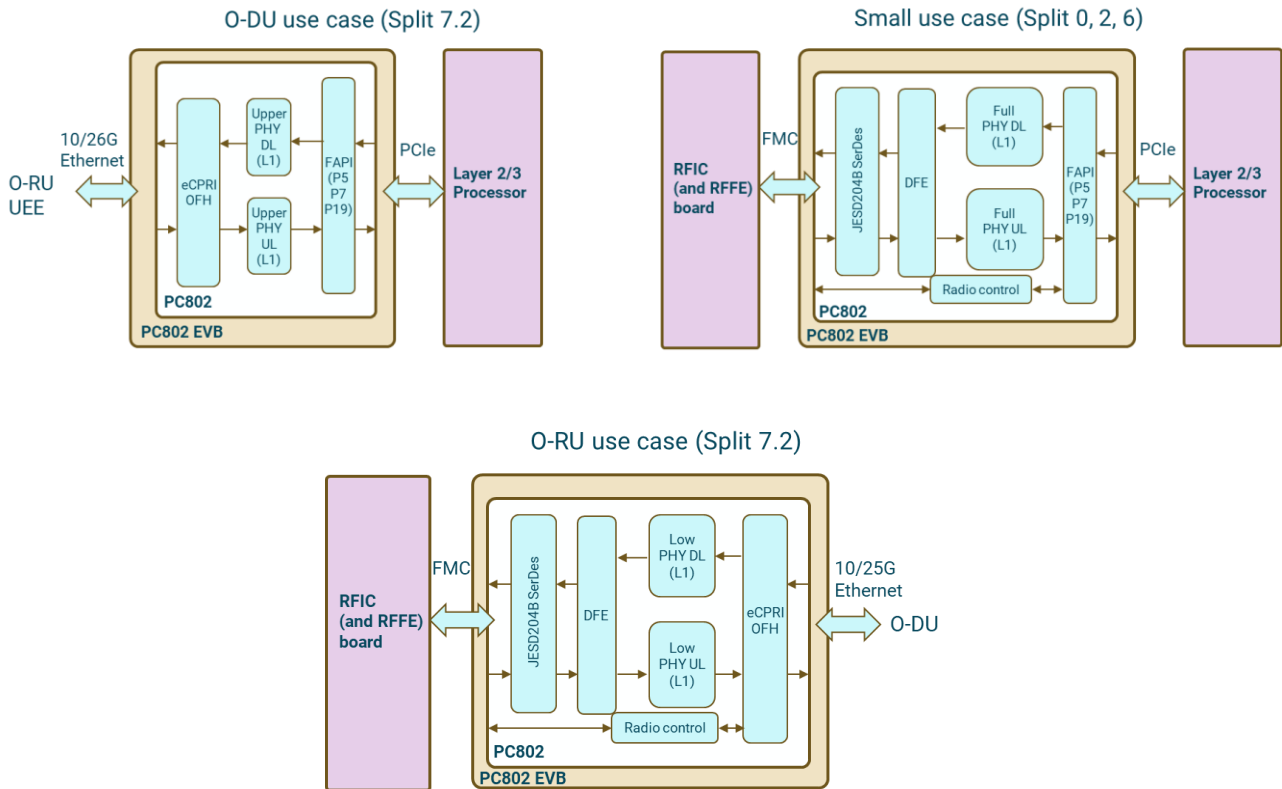
- ◆ PC802 SoC Silicon subsystem
- ◆ 2GB 32-bit interface LPDDR4 SDRAM
- ◆ JESD204B, SPI, GPIO and I2C interface from PC802 to RFIC FMC connector
- ◆ Jumper and switches for RFIC configuration
- ◆ 4 on-board temperature sensors
- ◆ Two boot modes: Normal and Debug
- ◆ Synchronisation and clocking functions

Key Interfaces and Connectors

- ◆ 4-lane JESD204B radio interface supporting up to 4 RFICs
- ◆ SFP28 cage for optical LC 10/25GE interface for O-RAN open fronthaul eCPRI
- ◆ Gen 4 4-lane PCIe interface carrying SCF FAPI interfaces to/from external NPU or PC
- ◆ FMC connector to FR1 ADI ADRV902x or TI AFE7769 RFIC Evaluation board (PN PC802EVK-1). Contact Picocom for support.
- ◆ Micro USB UltraSoC debug port (cable provided)
- ◆ Micro USB console port via UART interface
- ◆ GNSS Antenna port
- ◆ Auxiliary 12V power supply port (cable provided)
- ◆ Additional debug, clock and test connectors



Use Case Diagrams



Further Information

The following documentation as part of the product release, additional design documentation such as schematics and .brd design files are available.

- ◆ PC802 Evaluation Board (PC802EVB) Reference Manual
- ◆ PC802 Evaluation Board (PC802EVB) User Guide

Other PC802 board-level products include:

- ◆ PC802 Small Cell Development Board with on-board layer 2 NPU and ADI ADRV9029 RFIC
- ◆ ORANIC PHY and NIC for small cell O-DU with 4 PC802s

Please contact Picocom info@picocom.com for any additional information on the PC802 silicon, platforms, tools or software products.

Ordering Information

Part number	Product Name	Details
PC802EVB-1	PC802 Evaluation Board	Engineering samples, and software support package (on first board ordered)

The Export Control Classification Number (ECCN) is 5A991.b.4

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